

**AMENDMENT****IN THE SPECIFICATION:**

Please replace Paragraph [0013] with the following replacement paragraph:

**[0013]** On the other hand, pins (BP5, BP6, BP7 and BP8) of a buffer 140 can individually connect with data pins of the control processing unit 110 electrically, and also receive a control signal (CS) from a control processing unit, as is shown in the figure. While a memory 120 is being recharged, a control signal (CS) can enable the buffer 140 and let the buffer 140 in an active situation. While the buffer is in an active situation, the control processing unit 110 can feed an output signal (So) from a data pin P5 into the buffer 140, and transfer the output signal (So) through the buffer 140. That is, the buffer 140 is used as an output signal buffer, such as Model ~~TC74/HC374~~ TC74HC374 buffer, and data pins of a control processing unit (P5, P6, P7, and P8) are applied as the outlets of signals.